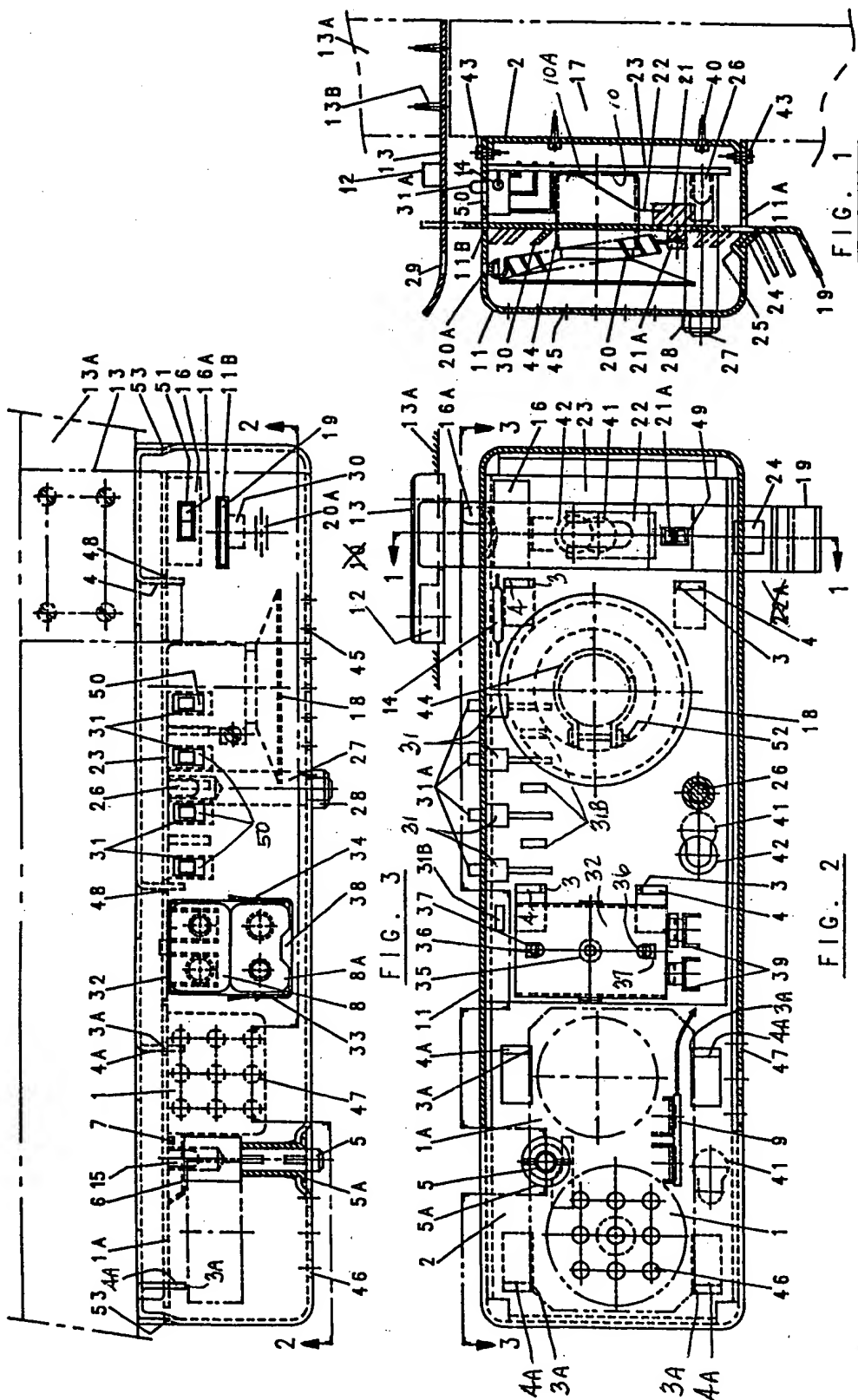




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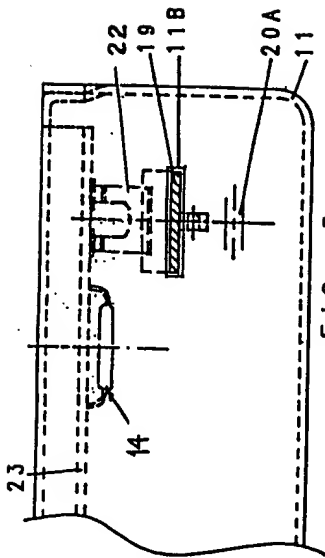


FIG. 5

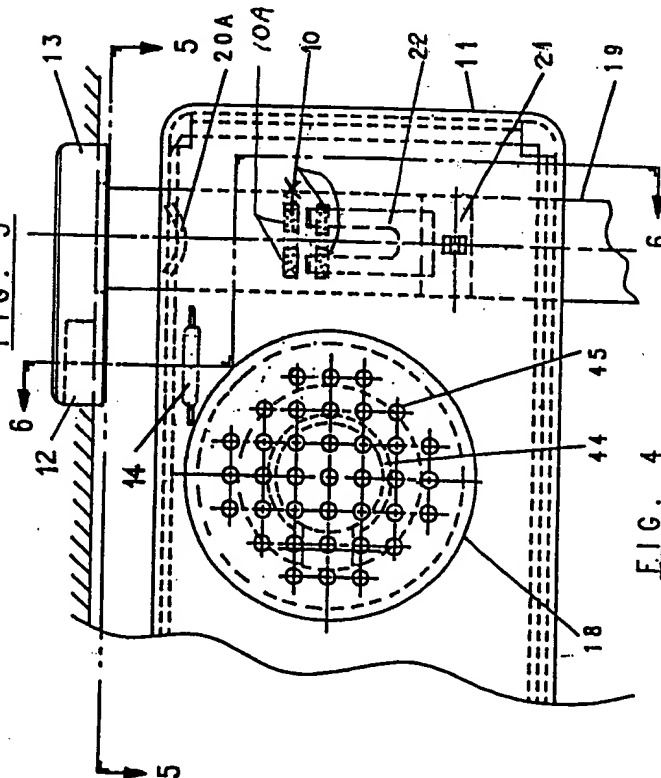


FIG. 4

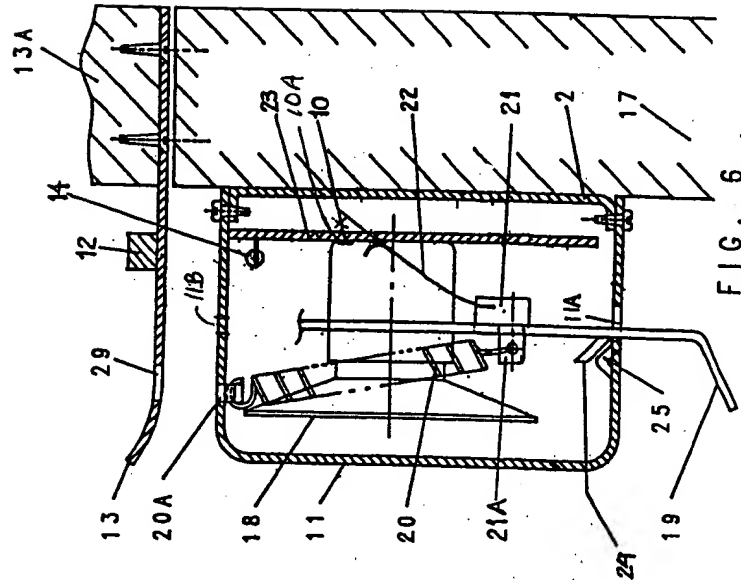


FIG. 6

**UP OUTPUTS**

ALRM SIGNAL	SW1 MODE SWITCH CHIME/ALARM
CHIM CHIME SIGNAL	S1 PREALARM SWITCH ON PCB
AUPOIN TURN ON US	S3 BOLT SWITCH HOME ARMED ON PCB
ALARM LED BLINK	S2 DOOR SENSOR SWITCH (REED)
LOBLED FOR LOW BATTERY BLINK LED	JMP1 ALARM DELAY
	LOBATT LOW BATTERY SIGNAL

**UP INPUTS**

SW1 MODE SWITCH CHIME/ALARM  
S1 PREALARM SWITCH ON PCB  
S3 BOLT SWITCH HOME ARMED ON PCB  
S2 DOOR SENSOR SWITCH (REED)  
JMP1 ALARM DELAY  
LOBATT LOW BATTERY SIGNAL

16 MODE SW IN CHIME MODE (MAGNET ACTUATED)  
10, 22 PRE ALARM SW  
14 DOOR SENSOR NC (MAGNET ACTUATED)  
10A, 22 BOLT SW

FIG. 7